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**Deliverable 2.4: Report on squaring along (110) orientation assessment for manufacturability**

**Task 2.1: Developing new cost-effective and high-quality wafer manufacturing processes**

WP2: Ingot & Wafer

**Lead participant:** CEA **Delivery date:** 30/04/2024 **Dissemination level:** Public **Type:** Report



# **Document History**





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### **Project Partners**





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### <span id="page-5-0"></span>**Executive Summary**

CEA carried out the development of the 45° squaring of ingots and initiated collaboration on the topic with the two industrial partners Kalyon (p type boron doped) and Norsun (p type gallium doped). According to wafers size constraints of the partners, two brick sizes has been successfully manufactured in such alternative configuration: a p-type Cz ingot doped with boron from Kalyon for G1 45° oriented brick squaring and a ptype Cz ingot doped with gallium from Norsun for M2 45° oriented brick squaring. For both configurations the (110) plane misorientations after grinding were lower than 0.3° which allows a mechanical cleavage between the metallization busbars of the future PV cells. Moreover, excellent results were obtained on the 45° wafers produced after integration in the different cell and module production lines (INES Labfab, and Kalyon Gigawatt Factory). In particular:

- No increase in breakage rate with this new wafer orientation was observed compared to standard Cz wafers usually integrated.
- Similar final efficiencies measured on the G1 45° oriented PERC cells processed by Kalyon when compared to standard production values observed in the factory.



# <span id="page-6-0"></span>**1 Introduction**

<span id="page-6-1"></span>A clean half-cell separation process was elaborated at CEA. It is based on 45◦ rotated silicon brick and wafers fabrication, allowing the alignment of the preferential cleavage plan (110) with the solar cells bus bars. Consequently, the cell separation can be simply realized thanks to a short diamond tip scribe at the edge, followed by a mechanical bending stress to propagate a crack along the wafer, parallel to the cell's metal bus bars [1]. Therefore, no laser-assisted technique is needed, and the resulting edge appears as smooth as what can be obtained with the use of recent thermo-mechanical cleavage solutions proposed by the industry (TLS by 3D-Micromac or LDC by INNOLAS). Such superior edge morphology obtained thus with the 45° rotated wafers developed is thus particularly well adapted for the further application of efficient cut-edge (re)passivation solutions [2-3]. However, manufacturability and scaling-up of such cut and passivation solutions must be now evaluated in real production environment conditions, and joint evaluation results achieved by CEA, NORSUN and KALYON for this technology will be reported in this report.



### **2 Context**

Half-cells or shingle module architectures have widely and quickly spread on the PV market to both ensure continuous increase of solar panel power (reduced resistive losses and increase of the usable area of the module) and new demands for adaptive PV modules (BIPV, VIPV,.etc), while implying minimum technology alteration [4]. However, the electrical gain obtained at module level using half-cell or shingle configuration may be lessened by absolute cell efficiency loss resulting from half-cell cutting process or sub-cells in the case of shingle module architecture. Two main approaches are currently considered at industrial scale for half-cell cutting process: i) conventional laser scribe and break and ii) thermo-mechanical cleavage (TMC), where a controlled thermal gradient is used to propagate the cleavage crack initiated by laser shallow scribing on the edge of the wafer. The cutting step process generally induces edge defectivity which is especially impacting for low temperature cell architectures such as silicon heterojunction (SHJ) solar cells. Today, for any of the considered optimized cutting processes, the absolute cell efficiency loss is in the range of  $\sim$ 0.2 to 0.3% $_{\text{Abs}}$  for high efficiency SHJ when going from full to half-cell configuration [5-6] and can reach up to close to  $~1\%_{\rm Abs}$ loss in case of shingle configuration. Furthermore, with laser scribe and cleave approach, the local melting of the silicon cut-surface-induced surface morphology that usually totally avoids any application of edge repassivation process that may be applied to recover for the initial cut-cell efficiency losses. During the past years, CEA studied an alternative approach for solar heterojunction cell separation, based on the fabrication of the so-called 45° rotated wafers. For such wafers, the (110) privileged crystallographic planes are naturally aligned with the cell edges (Figure 1). Indeed, a standard silicon brick is generally shaped along (010) lateral planes and consequently the (110) preferential cleavage plane is located on the resulting wafer's diagonals. In our case, thanks to a simple 45 $\degree$  ( $\pm$  0.1 $\degree$ ) rotation of the ingot during the squaring step applied to form the final brick, we modify the orientation of such cleavage plane and generate new wafer scheme with final (110) oriented lateral planes. Hence, we allow alignment of the preferential mechanical cleavage plane of the corresponding 45◦ oriented wafers parallel to the wafer edges (Figure 2), and most importantly, parallel to the metal pattern busbars where future cut is needed. This new wafer orientation allows the end user to get fully rid of laser assisted cleavage technologies, as simple mechanical edge scribing followed by mechanical cleavage is now possible, leading to final high edge morphology quality.

No modification of process is needed for the generation of 45° rotated wafers. Standard squaring and wafering process steps can then be applied to such ingots, with in particular the use of usual diamond wiresawing equipment to generate the wafers. The resulting 45◦ rotated 160 μm thick M2 (156.75 mm large) wafers were then successfully integrated on the industrial CEA-INES pilot line for the fabrication of SHJ solar cells [7-8]. No adaptation or modification of any fabrication step was necessary, and equivalent efficiencies and breakage rate observed for the newly 45° wafers when compared to the usual Cz references.

In the frame of IBC4EU project CEA supported NORSUN and KALYON to test and adapt this 45° rotated process to their own squaring and wafering systems with larger-compliant bricks. The objective being to validate that this approach can be fully compliant with full production environment and larger ingot/wafer sizes.





<span id="page-8-1"></span>Figure 1: Brick orientation and fabrication of 45<sup>°</sup> rotated wafers, the (110) plane is set parallel to the wafer edges compared to a diagonal direction for standard wafers.



<span id="page-8-2"></span>



<span id="page-8-3"></span><span id="page-8-0"></span>Figure 3: SEM pictures of the 45° rotated cut solar cells edges: a. global view of the edge b. zoom closer to the front surface. Both SEM show very smooth and uniform edge final cut morphologies.



# **3 45° squaring principle**

In this paragraph, the different fabrication steps of the 45° squaring are presented. It is good to note that the methodology presented here is in line with the equipment available at CEA: it is a "semi-manual" R&D mode compared to what is done today in the industry.

# **3.1 Step 1: Ingot section marking**

<span id="page-9-0"></span>The objective of step1 is to mark the position of the (110) plane on the section of the ingot (Figure 4). To do this, it is necessary to locate the position of the 4 nodes on the surface of the ingot which are characteristics of a (100) Cz growth. The (110) plane are situated along "node1–node3" or "node2- node4" lines or parallels to these lines (figure 4). At this step, the mark made, representing the side of the brick, takes into account a margin of ~8 mm compared to the final size of the brick.



**Figure 4:** (110) plane marking on the ingot section

## <span id="page-9-2"></span>**3.2 Step 2: squaring**

<span id="page-9-1"></span>The squaring of the brick is performed with a "B.E.A" squarer using 0.6 mm diameter diamond wire (Figure 5). It is possible to cut two sides of the brick per cutting run, two cutting runs are thus necessary to square the brick.





**Figure 5:** a. B.E.A squarer equipment (CEA) b. 45° oriented ingot squaring

### <span id="page-10-2"></span>**3.3 Step 3: initial angle misorientation measurement**

<span id="page-10-0"></span>Initial angle misorientation with the plane (110) are measured with a goniometer (Figure 6). From this measurement, the correction to be applied at the grinding step is computed.



**Figure 6**: Goniometer equipment (CEA) – (110) plane misorientation measurement

### <span id="page-10-3"></span>**3.4 Step 4: Grinding**

<span id="page-10-1"></span>The grinding of the brick is performed with an "Arnold" grinding equipment (Figure 7). A wedging of the brick is carried out (up to 4mm) to take into account the correction to be applied (determined in the previous step). With each pass it is possible to remove up to 300 µm of silicon. Once the final size of the brick is reached, two "finishing" passes at 20  $\mu$ m and 10  $\mu$ m are carried out on each face of the brick.





**Figure 7:** a. Arnold grinding equipment (CEA) b. 45° oriented brick grinding

<span id="page-11-1"></span>According to the equipment available at CEA it is possible to square 45° oriented bricks until M6 size. The following table summarizes the different wafer formats that can be processed.

<span id="page-11-2"></span>

#### **Table 1**: Possible brick size fabrication at CEA.

<span id="page-11-0"></span>After the grinding step, goniometer measurements are performed on the 4 sides of the brick to control the final angle misorientation with the (110) plane. The objective is to achieve a final misorientation of less than 0.3°.



# **4 Kalyon Ingot**

Kalyon sent to CEA a 50 cm piece of p-type Cz ingot doped with boron for 45° oriented brick squaring. CEA prepared a G1 45° oriented brick and sent it back to Kalyon for wafering, cell processing and mini modules manufacturing. This CEA-Kalyon collaboration allowed thus to evaluate the potential of the 45° oriented wafers in a full industrial environment for most of the production chain.

## **4.1 45° ingot squaring G1 size**

<span id="page-12-0"></span>The G1 45° oriented brick squaring (figure 8) followed the principle presented in chapter 3. After the squaring step the initial misorientation to be corrected was in the range of +1.19° to 1.46° (Table 2). After grinding the final (110) angle misorientation finally obtained was in the 0.14° to 0.27° range which corresponds to a deviation of the cleavage plane along the cell side of 387µm to 730µm.



**Figure 8:** G1 45° oriented brick, p-type boron doped Cz ingot provided by Kalyon

<span id="page-12-2"></span>

#### <span id="page-12-1"></span>**Table 2**: (110) plane misorientation measurements after squaring and grinding steps - Kalyon



### **4.2 G1 45° oriented brick in Kalyon industrial line**

<span id="page-13-0"></span>The G1 45° oriented brick was sent to Kalyon for wafering, PERC cell processing and PV mini-module manufacturing in their production line. Excellent results were achieved on the 45° wafers produced:

- the G1 45° oriented brick was cut into 175 µm thick wafers. No increase in breakage rate with new wafer orientation was observed compared to standard wafers (Figure 9),
- the efficiency of PERC cells generated is similar to standard production values observed in the factory (Figure 10). Two groups of wafers were used: 45° oriented (45DR) and standard (STD) for reference (~200 wafers each),
- mini-modules each composed of two half cells were manufactured at Kalyon for mechanical and electrical testing. Here again no increase in breakage rate was observed compared to mini module manufacturing with standard wafers (Figure 11 & Table 3).



**Figure 9:** G1 45° oriented brick wafering - Kalyon

<span id="page-13-1"></span>

<span id="page-13-2"></span>**Figure 10:** PERC cell efficiency: comparison between 45° oriented wafers and standard wafers





**Figure 11:** Example of mini-module made with two 45° oriented PERC half cell - Kalyon

<span id="page-14-2"></span><span id="page-14-1"></span><span id="page-14-0"></span>

#### **Table 3**: Mini-modules electrical measurements - Kalyon



## **5 NORSUN Ingot**

Norsun sent CEA a 50 cm piece of p-type Cz ingot doped with gallium for 45° oriented brick squaring. CEA prepared one M2 45° oriented brick and one M2 standard brick. The two bricks have been wafered by CEA and ~200 wafers were sent to ISFH for Polo-IBC cell integration.

## **5.1 45° ingot squaring M2 size**

<span id="page-15-0"></span>The M2 45° oriented brick squaring (figure 12) followed the principle presented in chapter 3. After the squaring step the initial misorientation to be corrected was in the -1.21° to -1.35° range (Table 4). After grinding the final (110) angle misorientation was in the -0.02° to -0.13° range which corresponds to a deviation of the cleavage plane along the cell side of 55µm to 355µm.



**Figure 12:** M2 45° oriented brick, p-type gallium doped Cz ingot Norsun

#### <span id="page-15-1"></span>**Table 4**: (110) plane misorientation measurements after squaring and grinding steps - Norsun

<span id="page-15-2"></span>







### **5.2 Standard ingot squaring M2 size**

<span id="page-16-0"></span>To perform the (100) standard squaring it is necessary to locate the position of the 4 nodes on the surface of the ingot which are characteristics of a (100) Cz growth. The (100) plane are situated along "node1-node2" or "node2- node3" lines or parallels to these lines (figure 10.a). After the squaring and the grinding steps (figure 10.b) the (100) plane misorientations are in the range of -1.08° to -1.28° (Table 5). Here no angle correction was applied because the initial (100) plane misorientation achieved is in accordance with what is usually observed in industry.



<span id="page-16-1"></span>**Figure 13:** a.M2 (100) plane marking ; b.M2 standard oriented brick, p-type gallium doped Cz ingot Norsun



<span id="page-16-2"></span>



### **5.3 Bricks wafering**

<span id="page-17-0"></span>The two bricks were cut at CEA with the Meyer Burger DW288 sawing machine. For both bricks the diameter of the diamond wire used was 60 $\mu$ m and the target thickness of the wafers was 160  $\mu$ m.

During the wafering step no problem was observed. No increase in breakage rate with the 45 oriented wafer orientation was observed compared to standard wafer. Furthermore, wafering shows no impact of wafer orientation on wafer's topology (Table 6).

<span id="page-17-3"></span>**Table 6:** Wafer topology, comparison between M2 45° oriented wafer and standard wafers. Values averaged over 60 wafers- CEA





<span id="page-17-2"></span><span id="page-17-1"></span>Figure 14: Norsun M2 "standard" wafers a. wafer pre-cleaning; b. wafer cleaning - Novatec equipments



## **6 Conclusion**

An innovative cut-cell cleavage process was elaborated and validated at CEA at laboratory scale. This process is based on 45◦ rotation during silicon brick and wafers fabrication, allowing the alignment of the preferential cleavage plane (110) with the solar cells bus bars. In this IBC4EU project, CEA collaborated with industrial partners to the adaptation and compliance of this 45° approach to larger ingot/brick sizes and alternative doping configurations. Finally, full evaluation of the wafer produced with industrial integration requirements needed to be assessed. Two series of 45° oriented squaring were thus performed according to partners's constraints regarding wafer size for further PV cell integration, allowing the manufacturing of the two following bricks: a p-type Cz ingot doped with boron from Kalyon in G1 size and a p-type Cz ingot doped with gallium from Norsun in M2 size. For both configurations the (110) plane misorientations obtained after grinding were less than 0.3° which allows a pure mechanical cleavage between the metallization busbars of the future PV cells. Wafer preparation steps also demonstrate full compliance of the 45° rotation with existing production tools, as no increase of breakage rate was observed when compared to standard wafers.

<span id="page-18-0"></span>Finally, the G1 45° oriented p-type wafers produced were processed on Kalyon's PERC production line. Results obtained at both cell and module level are very promising, with similar final efficiencies and breakage rates achieved, demonstrating here again the very good compliance of the 45° rotated approach with full industrial environment requirements. With this deliverable, we thus show that the 45° rotated wafer based approach developed by CEA can be integrated in full production environment, can be compatible with large final wafer/brick size, and allows a significant simplification of the cell cut-step, opening new alternative technological solutions to the traditional laser-based processes usually considered.



# **7 Glossary**

<span id="page-19-0"></span>



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